

Feedback

$x \rightarrow \frac{\beta}{1+\beta A} x \rightarrow y = \frac{A}{1+\beta A} x \approx \frac{1}{\beta} x \quad G = \frac{1}{\beta}$

E. distortion

$V_{out} = V_{in} + \alpha_1 V_{in} + \alpha_2 V_{in}^2 + \dots$
 $V_{out} = D + A(V_{in} - V_{out})$
 $V_{out} = \frac{D}{1+A} + \frac{A}{1+A} V_{in} \approx V_{in} \text{ if } A \gg 1$

Feedback check

- 1. Shut down source
- 2. Remove opamp, connect V_{out} to V_{in}
- 3. See if any of V_{out} appears at V_{in}

Diodes

$i_d = I_s (e^{\frac{V_d}{nV_T}} - 1)$
 $\phi_T = \frac{kT}{q} = 26 \text{ mV}$
 $\frac{V_d - V_{d0}}{R} = i_d = I_s e^{\frac{V_d}{nV_T}}$

- 1. $V_d \sim 100 \text{ V} \rightarrow V_d \approx 0$ in FB $\rightarrow$ ideal diode
 - 2. $V_d \sim 10 \text{ V} \rightarrow$ drop 0.6 V , 0.5 , $1.5 \rightarrow$ fixed-drop model
 - 3. $V_d \sim 1 \text{ V} \rightarrow$ full Shockley a. numerical
- E. $R = 100, V_d = 1$ (1) guess $V_d = 0.5 \text{ V}$
(2) $i_d = \frac{1-0.5}{100} = 5 \text{ mA}$
(3) $V_d = 0.7 \text{ V}$
 $V_d = 0.687 \text{ V}$
- b. graphical Load line analysis E. $R = 1 \text{ k}, V_d = 1$
load line: $\frac{1-V_d}{1 \text{ k}} = i_d$ find intersection w/ Shockley

Non-lin apps 1. log

$V_{d1} = n\phi_T \ln(\frac{i_{d1}}{I_s})$ $V_{d1} + V_{d2} = n\phi_T \ln(\frac{i_{d1} i_{d2}}{I_s^2})$ $i_{out} = I_s e^{\frac{V_{d1} + V_{d2}}{n\phi_T}} = \frac{i_{d1} i_{d2}}{I_s}$

1. log $V \rightarrow i$ 2. sum $(\ln i)$ 3. exp $i \rightarrow V$

Input: offsetted sin waves. E. $V_1: 9 \text{ k}, V_2: 10 \text{ k} \Rightarrow V_{out}: 1 \text{ k}, 9 \text{ k}, 10 \text{ k}, 19 \text{ k}$
2. Temp $V_d = n\phi_T \ln \frac{i_d}{I_s}$ $V_d = V_{d0} - 2 \text{ mV/K} (T - T_0)$

Bandgap ref. $T \uparrow \rightarrow V_D \downarrow$, but $\Delta V_D \uparrow$, so we can weigh them and cancel V_D as $T \uparrow$

$V_d = n\phi_T \ln \frac{I}{I_s}$ $V_{d1} - V_{d2} = \frac{kT}{q} \ln \frac{I_{s2}}{I_{s1}}$

$V_{out} = V_{as} + A(V_{d1} - V_{d2})$
 $= V_{d0} - 2 \text{ mV/K} (T - T_0) + A \frac{kT}{q} \ln \frac{I_{s2}}{I_{s1}}$
 $= V_{d0} + 2 \text{ mV/K} T_0 + T [-2 \text{ mV/K} + \frac{A n k}{q} \ln \frac{I_{s2}}{I_{s1}}]$ tune it

Kujik bandgap $\ominus$ feed, since $\ominus$ fed, $\oplus$ clipped only by D_1 .

$V_p = V_n \rightarrow i_1 = i_2$ if $R_1 = R_2$
 $V_{out} = i_2 R_2 + V_{d1} = \frac{R_2}{R_1} (V_{d1} - V_{d2}) + V_{d1}$
 $= \frac{R_2}{R_1} (\frac{n k T}{q} \ln \frac{I_{s2}}{I_{s1}}) + V_{d0} - 2 \text{ mV/K} (T - T_0)$
 $= T [\frac{R_2}{R_1} (\frac{n k}{q} \ln \frac{I_{s2}}{I_{s1}}) - 2 \text{ mV/K}] + [V_{d0} + 2 \text{ mV/K} T_0]$

$n = 1.752$ for $1 \text{ N} 4148 \quad i_1 = \frac{V_{out} - V_{d1, worst}}{R_1} = 5 \text{ mA} \rightarrow R_1 = R_2 = 250 \Omega \rightarrow R_3 = 26.2 \Omega$
Find slope of V_{out} vs. T to recalculate $R_3 = 30.1 \Omega$

3. One-way DR: rectify RD: clip DC: peak def. DC+load: drop

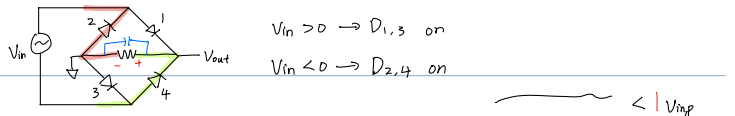
- Half-wave + I_{load} (E battery charger) $\rightarrow V_d \downarrow$ linear

$C V_R = \Delta Q = \int_{T_{dis}} I_L dt = T_{dis} I_L \rightarrow C = \frac{T_{dis} I_L}{V_R}$
if V_R small, $T_{dis} \approx T_p$, and for R load, discharge $\sim$ lin. $I_L \approx \frac{V_R}{R}$

- Full-wave

C only need hold half as long $C = \frac{I_L T_p}{2 V_R}$ Can't breakdown $< 2 V_{in}$

Bridge

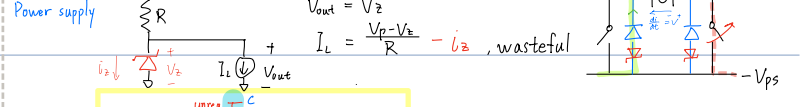


$V_{in} > 0 \rightarrow D_{1,3} \text{ on}$
 $V_{in} < 0 \rightarrow D_{2,4} \text{ on}$
 $I_{p, avg} = \text{avg } I_{load} \text{ (half)} \text{ or } \frac{1}{2} \text{ avg } I_{load} \text{ (full)}$ Long-term heating
2. peak $i_{peak} = C \frac{dV}{dt} + I_{load} \approx I_{load} (1 + 2\pi \frac{V_p}{2V_r})$

Precision rect. $V_{in} > 0$, no drop, $V_{out} = V_{in} = V_{oA} - V_D$
 $V_{in} < 0 \rightarrow$ pulled down to 0 $V_{oA} = V_{ss}$
 $R \rightarrow C \rightarrow$ prec. peak def.



DC motor driver Switch off $\rightarrow V = L \frac{di}{dt} = -2V_{ps} - 2V_D - 2V_z$
 $\rightarrow$ H-bridge D to block fwd current z for more drop.



Regulator (load peak detector) $V_{out} = V_z$
 $I_L = \frac{V_p - V_z}{R} - i_z$, wasteful
CCCS for more i_L drive

BJT $\alpha = \frac{I_c}{I_e} \approx \frac{I_c}{I_b + I_c} = \frac{\alpha}{1 - \alpha}$
E. Inverting $I_c = 1 \text{ k}$, inverting $\Rightarrow G$ solve
 $I_c = f(V_{BS}, V_{CE})$
Want to stay in active (E bias)

$I_B = \frac{V_{in} - V_{BE}}{10 \text{ k}} = \frac{I_c}{\beta = 100}$
 $I_B \rightarrow I_c$ $V_{BE} \approx 0.7 \text{ V}$
 $I_B \rightarrow I_c$ $V_{BE} \approx 0.7 \text{ V}$, better play w/ I_B

Approx. $V_{BE} = 0.75 \text{ V}$
 $V_{out} = V_{CC} - i_c R_2 = V_{CC} - (\beta \frac{V_{in} - 0.75}{R_1}) R_2 = V_{CC} - \frac{R_2}{R_1} \beta (V_{in} - 0.75)$
 $V_{out} = V_{CC} - \frac{R_2}{R_1} \beta (V_{BE} - 0.75) - \frac{R_2}{R_1} \beta \Delta V = -10 \Delta V$

Small-sig $I_a = I_s e^{\frac{V_d}{\phi_T}} = I_a (1 + \frac{\Delta V}{\phi_T})$
 $V_D = V_d + \Delta V \rightarrow i_D = I_a e^{\frac{V_d}{\phi_T}} = I_a (1 + \frac{\Delta V}{\phi_T})$
 $\Delta i = \frac{I_a}{\phi_T} \Delta V \equiv \frac{\Delta V}{r_e} \rightarrow$ small-sig emitter $R \equiv \frac{\phi_T}{I_a}$

1. Find bias point (nonlin) with V_W (DC) $\rightarrow V_{out}, I_D$
2. Turn off V_W , replace ∇ w/ $r_e = \frac{\phi_T}{I_c}$ $\frac{V_W - I_D r_e}{10 \text{ k}} + \alpha i_e = i_e$ $A_v = -\frac{\alpha (1 \text{ k})}{r_e + 10 \text{ k} (1 - \alpha)}$
3. Add $\Rightarrow V_{out} + V_{out} \quad V_{out} = (-\alpha i_e) \cdot 1 \text{ k} = (-\alpha \frac{V_{in}}{r_e}) \cdot 1 \text{ k}$

Hybrid π $\alpha i_e = i_c = g_m V_{be} \quad g_m = \frac{I_c}{V_T} = \frac{I_c}{\phi_T}$
 $i_e - i_c = i_b = \frac{V_{be}}{r_e} \quad r_e = \frac{\phi_T}{I_e} = r_e (1 + \beta) = \frac{\beta}{g_m}$

MOS Sym. device $V_{as} = 0$ Cutoff
 $0 < V_{as} < V_T$ Subthreshold
 $V_{as} > V_T$ Lin. small $v_{as} \rightarrow R$ controlled by V_{as}
 $g \propto V_{as} - V_T$

Triode channel pinched, $R \uparrow$, bad
Sat. $V_{as} \geq V_{as} - V_T$, all pinched, but ET, e-drift
 $i_D = \frac{1}{2} \mu C_{ox} \frac{W}{L} (V_{as} - V_T)^2 (1 + \frac{V_{as}}{V_A})$ for nonflat sat.
Lin. $i_D = V_{as} [k' \frac{W}{L} (V_{as} - V_T)]$

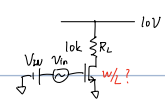
Amplifier Need V_{bias} for sat. (slope V_{out} vs V_{in}), $V_{as} = V_{bias} + \Delta V$
 $i_D = \frac{1}{2} k' \frac{W}{L} (V_{as} - V_T)^2 + k' \frac{W}{L} (V_{as} - V_T) \Delta V + \dots \Delta V^2$
 $= I_{bias} + g_m \Delta V = \frac{1}{2} k' \frac{W}{L} V_{as}^2 + g_m \Delta V$
 $V_{out} = V_{DD} - \frac{1}{2} k' \frac{W}{L} V_{DD}^2 R_L \quad V_{out} = -g_m V_{in} R_L$
 $\equiv V_{DD} - V_{DROPP} \quad \equiv -A_v V_{in}$
Design eq. $V_{DD} = \frac{2 V_{DROPP}}{A_v} \quad (A_v = g_m R_L = k' \frac{W}{L} V_{DD})$

E. $V_T = 1 \text{ V}, k' \frac{W}{L} = 44.4 \text{ mA/V}^2, A_v = -20, V_{swing} = \pm 2 \text{ V}, V_{DD} = 10 \text{ V}$
Let $V_{out} = 5 \text{ V} \rightarrow V_{DROPP} = 5 \text{ V} \rightarrow V_{DD} = \frac{2 V_{DROPP}}{A_v} = 0.5 \text{ V} \rightarrow V_{as} = 1.5 \text{ V}$
 $|A_v| = g_m R_L \rightarrow R_L = 900 \Omega$

E. Find $\frac{W}{L}$, given $A_v = 20$, swing $\pm 2V$, $k' = 30 \mu A/V^2$, $V_T = 0.3V$

Choose $R_L = 10k$, $V_{DD} = 5V \rightarrow I_D = 500 \mu A$
 $|A_v| = g_m R_L = \sqrt{2k' \frac{W}{L} I_D} R_L \rightarrow \frac{W}{L} \approx 133.3$
 Safer to bias I_D , not V_{GS} (V_{DD}), due to $\sqrt{\quad}$

Bias I_{BIAS} pulls down V_S until V_{GS} is appropriate
 indep. of V_T



Diode $i_D = I_S (e^{V_D/n\phi_t} - 1)$

$$\phi = \frac{kT}{q} = \frac{1.38E-23 J/K \cdot 300K}{1.6E-19 C} = 25.875 mV$$

$$V_D = n\phi_t \ln\left(\frac{i_D}{I_S} + 1\right)$$

$$V_{D1} - V_{D2} = n \frac{kT}{q} \ln \frac{I_{S2} I_{D1}}{I_{S1} I_{D2}}$$

$$V_D \text{ also } = V_{D0} - 2m(T - T_0)$$

rect $C = \frac{I_L T_P (1/2)}{V_{RIP}}$

BJT $\alpha = \frac{I_C}{I_E} = \frac{\beta}{1+\beta}$ $\beta = \frac{I_C}{I_B} = \frac{\alpha}{1-\alpha}$

E. $I_{BIAS} = R_S$ (variable) Given $V_T = 1V$, $k' \frac{W}{L} = 44.4 \mu A/V^2$, $V_{DD} = 10V$

want $A_v = -20$, swing $\pm 2V$, $V_{DD} = 5V$, $f_{min} = 20 Hz$
 $\rightarrow V_{GS} = \frac{2V_{DSAT}}{|A_v|} = 0.5V$, $V_{GS} = V_{GS} + V_T = 1.5V$, $R_S = \frac{V_{GS}}{I_D} = 900 \Omega$, $I_D = \frac{1}{2} k' \frac{W}{L} V_{GS}^2 = 5.55 mA$

Swing $V_{D,min} = 3V$, need $V_{GS} \leq V_T$ for sat.

$$V_{GS} = V_{GS} + V_{DS} \rightarrow V_{GS,min} = V_{DD} = 0.5V$$

Bias Choose $V_S = 1V \rightarrow R_S = \frac{V_S}{I_D} = 180 \Omega$
 $V_{GS} = 2.5V \rightarrow$ make $R_1 = 75k$ $R_2 = 25k$ (look ~ 100k)

Filter HPF to block DC

$$C_{in} f_{in} = \frac{1}{2\pi C_{in} (R_1 || R_2)} = \frac{f_{min}}{10} \rightarrow C_{in} \approx 4.24 \mu F$$

$$C_S g_m (V_G - V_S) = i_D = V_S \frac{1 + g_m R_S}{g_m R_S} \quad \text{(classical approach)}$$

corner at $1 + g_m R_S = \omega R_S C_S \rightarrow C_S \approx 2.2 mF$ (R_S only 180Ω)
 C_S will be much smaller if $R_S \uparrow \rightarrow \text{sat}$

if no C_S , feedback at both AC/DC ($V_{GS} \uparrow \rightarrow i_D \uparrow \rightarrow V_{GS} \downarrow \rightarrow \dots$)

$$V_{out} = -(g_m V_{GS}) R_D$$

$$A_v = \frac{V_{out}}{V_{in}} = \frac{-g_m R_D}{1 + g_m R_S} \rightarrow \text{neg. feedback, but improves distortion at same output swing}$$

$$\approx -\frac{R_D}{R_S} \text{ if } g_m R_S \gg 1$$

PMOS pull G_S down for I_D

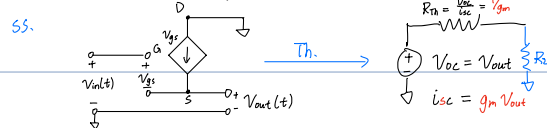
$I_D > 0$ for $V_{SG} > |V_{TP}|$ and $V_{SD} > 0$

$$I_D = \frac{1}{2} k_p' \frac{W}{L} (V_{SG} - |V_{TP}|)^2$$

Load $|A_v| = g_m R_D \frac{R_L}{R_D + R_L} = g_m (R_D || R_L)$ (divided)

Buffer (follower) (HW)

$I_D = \text{const.} \rightarrow i_D = 0 \rightarrow V_{GS} = 0 \rightarrow V_{out} = V_{in}$



$$V_L = V_{in} \frac{R_L}{R_L + 1/g_m} \quad 1/g_m \sim 10 \Omega \text{ small}$$

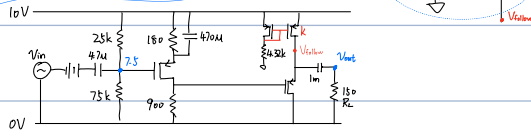
$$W_{corner} = C_{out} (R_L + 1/g_m)$$

Make sure I_D swing remains active ($I_{BIAS} \pm \frac{V_{out}}{R_L}$), else distorted

I_{BIAS} setup ($V_{SG} = V_{SD} \rightarrow I_D$)

R just to set V_{SG} for $k \times PMOS \rightarrow$ mirror

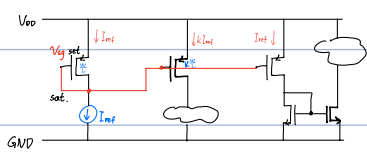
PMOS implementation



Current mirror I_{ref} to all analog cells

$I_{ref} \rightarrow V_{SG} = V_{SG, others} \rightarrow I_{others}$

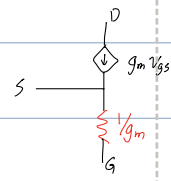
Need to ensure sat.



Alt ss. model (easier math)

$1/g_m$ cancels $\downarrow$, so i_g remains 0.

ss V_{GS} DC V_{GS} tot V_{GS}



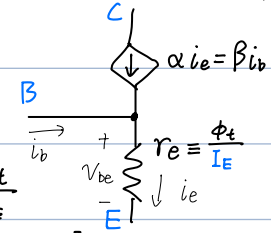
N, P (up, down?)

S/E may not be 0

$$V_p = \sqrt{2} V_{rms}$$

$$V_{OUT} = V_{CC} - \frac{R_2}{R_1} \beta (V_{DD} - 0.75) - \frac{R_1}{R_2} \beta \Delta V_{out}$$

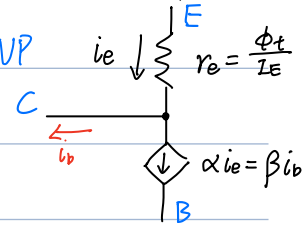
SS NPN



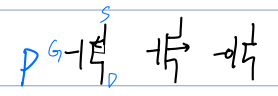
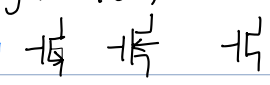
$$r_e = \frac{\phi_t}{I_E}$$

$$\pi \quad g_m = \frac{I_C}{\phi_t}, \quad r_\pi = \frac{\beta}{g_m}$$

PNP



MOS



active $V_{GS} - V_T > 0$

$V_{SG} - |V_{TP}| > 0$

sat $V_{DS} \geq V_{GS} - V_T$

$V_{SD} \geq V_{SG} - |V_{TP}|$

$$i_D = \frac{1}{2} k' \frac{W}{L} (V_{GS} - V_T)^2$$

$$g_m = \sqrt{2k' \frac{W}{L} I_{BIAS}}$$

w/ load $|A_v| = g_m (R_D || R_L)$

